



Appl. No. 10/525,996  
Election dated July 25, 2006  
Reply to Restriction Requirement of January 26, 2006  
Attorney Docket No. 1217-050618

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/525,996 Confirmation No. 6221  
Applicants : Hideo SENOO et al.  
Filed : February 28, 2005  
Title : Protective Structure of Semiconductor Wafer, Method for  
Protecting Semiconductor Wafer, Multilayer Protective  
Sheet Used Therein, and Method for Processing  
Semiconductor Wafer  
Art Unit : 2811  
Examiner : Cuong Quang Nguyen  
Customer No. : 28289

ELECTION

MAIL STOP AMENDMENT  
Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action dated January 26, 2006, in which a shortened statutory period for reply was set for one month. A five-month Petition for Extension of Time and requisite fee accompany this response.

**Election of Claims** begins on page 2 of this paper.

**Remarks** begin on page 3 of this paper.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 25, 2006.

Diane Paull

(Name of Person Mailing Paper)

*Diane Paull*  
Signature

07/25/2006  
Date